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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jaroslaw J. Sydir et al.

Title: Method and Apparatus for Calculating CRC on Data Using a Programmable CRC Engine

Docket No.: INTEL-015PUS

Serial No.: 10/749,128

Filed: December 30, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2133

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 2 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 50-0845.

Daly, Crowley & Mofford, LLP, c/o PortfolioIP
P.O. Box 52050, Minneapolis, MN 55402 (781-401-9988)

By: Paul D Durkee
Atty: Paul D Durkee
Reg. No. 41,003

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12th day of July, 2004.

Andrew J. Kaul
Name

[Signature]
Signature



Serial No. 10/749,128

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|-------------|--|-----------------|--------------|
| Applicant: | Jaroslaw J. Sydir et al. | Examiner: | Unknown |
| Serial No.: | 10/749,128 | Group Art Unit: | 2133 |
| Filed: | December 30, 2003 | Docket: | INTEL-015PUS |
| Title: | Method and Apparatus for Calculating CRC on Data Using a Programmable CRC Engine | | |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 50-0845 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Page 2

Serial No :10/749,128

Dkt: P17942 (INTEL)

Filing Date: December 30, 2003

Title: Method and Apparatus for Calculating CRC on Data Using a Programmable CRC Engine

Assignee: Intel Corporation

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

JAROSLAW J. SYDIR ET AL.

By their Representatives,

Daly, Crowley & Mofford, LLP
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P.O. Box 52050
Minneapolis, MN 55402
781-401-9988

Date July 12, 2004

By Paul D Durkee
Paul D Durkee
Reg. No. 41,003

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Andrew J. Kaul
Name

[Signature]
Signature



Under the Paperwork Reduction Act of 1996, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

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|--|-----------------------------|----------------------------------|
| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | <i>Complete if Known</i> | |
| | Application Number | 10/749,128 |
| | Filing Date | December 30, 2003 |
| | First Named Inventor | Sydir, Jaroslaw J. |
| | Group Art Unit | 2133 |
| | Examiner Name | Unknown |
| Sheet 1 of 1 | | Attorney Docket No: INTEL-015PUS |

| US PATENT DOCUMENTS | | | | | | |
|---------------------|---------------------|------------------|---|-------|----------|----------------------------|
| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |

| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|---------------------|------------------|---|-------|----------|----------------|
| Examiner initials* | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T ² |

| OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS | | | |
|--|----------------------|---|----------------|
| Examiner initials* | Cite No ¹ | include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | "Intel IXP2400 Network Processor for OC-48/2.5 Gbps network access and edge applications", Product Brief,(2003), 6 pages | |
| | | "Intel IXP2800 Network Processor For OC-192/10 Gbps network edge and core applications", Product Brief,(2002), pp. 1-6 | |

EXAMINER

DATE CONSIDERED